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PTO/SB/21 (6-98) (modified)  
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	<b>Application Number</b>	09/854,146	
	<b>Filing Date</b>	05/11/2001	
	<b>First Named Inventor</b>	Jun Li	
	<b>Group Art Unit</b>	2825	
	<b>Examiner Name</b>	Siek, Vuthe	
<b>Total Number of Pages in This Submission</b>		<b>Attorney Docket Number</b>	SPLX.P0050

Enclosures (Check all that apply)		
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<b>Firm Or Individual name</b>	John Stattler, Reg. No. 36, 285 of Stattler Johansen & Adeli LLP
<b>Signature</b>	
<b>Date</b>	July 24, 2003

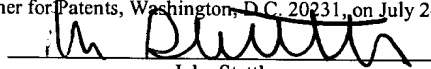
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Docket No.: SPLX.P0050

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John Stattler

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Jun Li

Serial No.: 09/854,146

Filing Date: 05/11/2001

For: HIGH ACCURACY TIMING MODEL  
FOR INTEGRATED CIRCUIT

Examiner: Not yet assigned

Group Art Unit: 2812

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313

Sir:

In response to the Office Action dated January 24, 2003, please amend the patent application as follows:

**IN THE SPECIFICATION:**

On page 8, lines 1-14, please replace the existing paragraph to read:

The voltage signals input to a circuit are characterized by input signal slew rate.

In general, the input signal slew rate measures the rate at which the voltage rises from

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